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Application No.	Applicant(s)	
10/798,416	STANICK ET AL.	
Examiner	Art Unit	
Terrence R. Willoughby	2836	
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DETAILED ACTION

**Reasons for Allowance** 

1. Examiner acknowledges a submission of the amendment filed on March 4, 2006.

Claims 1 and 5 have been amended. Claims 2-4 and 6-7 have been canceled. No new

claims were added. It also included remarks/arguments. Amendments and arguments

have overcome rejection.

2. The following is an examiner's statement of reasons for allowance:

a) Modular Solid State Relay, Jones et al. (US 5,463,252)

b) Digital Output Control Device and Method for Operating, Jenets et al. (US

5,537,285)

3. Regarding claims 1 and 5, the prior art references fail to teach "a D.C/D.C. power

converter that regulates standard locomotive voltage by stepping down the locomotive

voltage to a solid-state relay control side voltage; an internally and externally self-biased

solid-state relay device for the relay assembly, wherein a normally open configuration to

close, causes said normally closed configuration to open, wherein the opening of said

internally self-biased solid-state relay device causes said externally self-biased solid-

state relay device to be no longer self-biased, thus opening said externally self-biased

solid-state relay device" in combination with the other limitations recited in the claims.

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4. The following US patents: Jones et al. describes a relay assembly comprising a plurality of solid-state relay devices, wherein each of the solid-state relay devices has a switch, and a digital signal regulated by a D.C./D.C. power converter; wherein the switch comprises a load side, and said signal comprises a control side opposite of the load side. Jenets et al. Describes a solid-state relay module suitable for accepting lowlevel digital signals from a digital computer, and for providing on/off control of heavy electrical loads. However, the prior art references does not disclose the capability for operating on standard higher locomotive voltages.

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5. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

## Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Terrence R. Willoughby whose telephone number is 571-272-2725. The examiner can normally be reached on 8-5pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Brian Sircus can be reached on 571-272-2800 ext. 36. The fax phone

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number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

TRW 7/5/06

> STEPHEN W. JACKSON PRIMARY EXAMINED

Hesten Jackson 7-10-06

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